



IFW

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s) Joseph P. Dower
Kultaransingh N. Hooghan

Case: 1-1

Serial No. 10/695193 Group Art Unit: 2826

Examiner Evan T. Pert

Filing Date 10/28/2003

Title: Fabricating Semiconductor Chips

Commissioner for Patents
Alexandria, VA 22313-1450

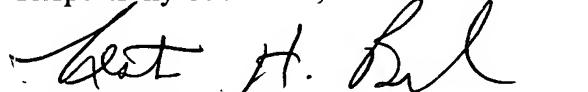
SIR:

Enclosed is an amendment in the above-identified application.

No additional fee is required.

In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit Deposit Account No. 501735 as required to correct the error.

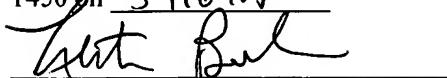
Respectfully Submitted,



Lester H. Birnbaum
Reg. No. 25830
Attorney for Applicants
610-530-9166

Dated: 5/16/05
Lester H. Birnbaum
2159 Greenmeadow Drive
Macungie, PA 18062

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents Alexandria, VA 22313-1450 on 5/16/05



5/16/05

Dower 1-1



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Inventor(s) Joseph P. Dower
Kultaransingh N. Hooghan

Case 1-1

Serial No. 10/695193 Group Art Unit 2826

Filing Date 10/28/2003

Examiner Evan T. Pert

Title Fabricating Semiconductor Chips

COMMISSIONER FOR PATENTS
ALEXANDRIA VA 22313

SIR:

AMENDMENT

In response to the Office Action dated 3/22/2005, please amend the above-identified application by replacing the claims as shown below.